



PATENT

In re Application of:  
Takashi KOBAYASHI  
Serial No.: 10/020,573  
Filed: October 31, 2001  
Title: VERTICAL INSULATED GATE  
SEMICONDUCTOR DEVICE  
Group Art Unit: 2826  
Examiner: Nathan Flynn  
Attorney Docket No.: FUJI:200

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Marc A. Rossi

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RESPONSE TO NOTICE REGARDING DRAWINGS

Sirs:

Please substitute the attached one sheet of formal drawing 24/34 depicting Figure 28 for the drawings filed on March 11, 2002.

Respectfully submitted,  
ROSSI, KIMMS & McDOWELL LLP

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